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1/2 ページ

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 04-113766

(43)Date of publication of application : 15.04.1992

(51)Int.Cl.

HQ4N 1/028

H04N 5/335

(21)Application number : 02-232405

(71)Applicant : CANON INC

(22)Date of filing : 04.09.1990

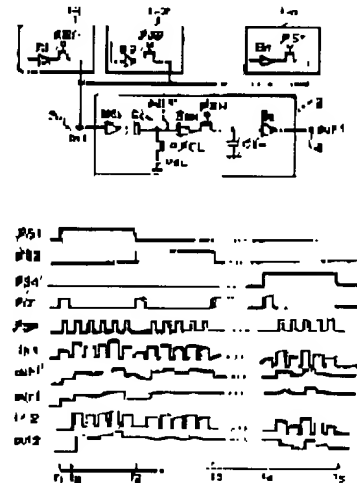
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(54) SOLID-STATE IMAGE PICKUP DEVICE

(57)Abstract:

PURPOSE: To eliminate an offset dispersion in an output buffer amplifier between chips by inputting a dummy signal in common to each chip to an output buffer amplifier of each chip at a head of each chip, clamping an output of the buffer amplifier while a dummy signal is read and sampling and holding the output.

CONSTITUTION: When a switch SW1 is opened from a time $t=t_1$ till $t=t_2$, a signal in1 of a chip 1 is inputted to an input terminal 3 of an output circuit 1. Since a clamp pulse ϕ_{CL} goes to H from a time $t=t_1$ till $t=t_6$, the clamp level VCL is outputted to an output terminal 4 as an out1'. Thus, a signal output with an offset of a buffer amplifier B1 added thereto is inputted to an input terminal 3 and while a dummy signal is read, since the VCL is outputted to the out1' a sum of the VCL and a difference between the dummy signal and a signal of each picture element is outputted as the out1' at read of each picture element. Similarly, signals of chips 1-3-1-n are read as the out1' by eliminating dispersion in the offset.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's